

SystemVerilog Tutorial - Complete Guide

Prerequisites

- Basic understanding of digital logic
- Familiarity with hardware description languages (helpful but not required)
- Basic programming concepts

Learning Path Recommendations

- **For Hardware Designers:** Focus on Parts I, II, and VI
- **For Verification Engineers:** Emphasize Parts I, III, IV, V, and VII
- **For Complete Beginners:** Follow chapters sequentially
- **For Experienced Verilog Users:** Start with Chapter 2, emphasize Parts III-V# Test